



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Lawrence S. Uzelac, et al. Art Unit : 2116
Serial No. : 10/625,584 Examiner : James K. Trujillo
Filed : July 22, 2003
Title : METHOD AND APPARATUS FOR REDUCING SUB-THRESHOLD OFF
CURRENT FOR A REAL TIME CLOCK CIRCUIT DURING BATTERY
OPERATION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TERMINAL DISCLAIMER UNDER 37 C.F.R. §§3.73(b) AND 1.321(b)

Pursuant to 37 C.F.R. § 3.73(b), INTEL CORPORATION, certifies that it is the assignee of the entire right, title, and interest in the above-referenced application by virtue of assignments from the inventors of the patent application referenced above. The first assignment was recorded at Reel 014594 Frame 0045 on October 14, 2003. The second assignment was filed in the Patent and Trademark Office on April 6, 2005. INTEL CORPORATION also certifies that it is the assignee of U.S. Patent No. 6,611,918.

The undersigned has reviewed all the documents in the chain of title of the above-referenced application and to the best of undersigned's knowledge and belief, title is in INTEL CORPORATION.

The undersigned is empowered to act on behalf of the assignee.

Pursuant to 37 C.F.R. § 1.321(b), and to obviate a double patenting rejection, the assignee referenced above hereby waives and disclaims the terminal portion of the term of the entire patent to be granted upon the above-referenced application subsequent to the expiration date of U.S. Patent No. 6,611,918, provided that any patent granted on the above-referenced application

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit May 6, 2005
Signature *R. P. Ippolito*

Roxanne Ippolito
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~~05/10/2005 WASFAW1 00000004 10325584~~

~~02 FC:1814~~

05/11/2005 WASFAW1 00000008 10625584

02 FC:1814

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shall be enforceable only for and during such period that it is commonly owned with U.S. Patent No. 6,611,918.

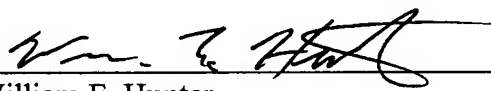
The assignee referenced above does not disclaim any terminal part of any patent granted on the above-referenced application prior to the expiration date of the full statutory term of U.S. Patent No. 6,611,918 in the event that it later: expires for failure to pay a maintenance fee, is held unenforceable, is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), has all claims cancelled by a reexamination certificate, or is otherwise terminated prior to expiration of its statutory term, except for the separation of legal title as stated above. Assignee herein does not disclaim or otherwise affect any part of U.S. Patent No. 6,611,918.

This disclaimer runs with any patent granted on the above-referenced application and is binding upon the grantee, its successors or assigns.

Enclosed is a check in the amount of \$130.00 for the disclaimer fee. Please charge any additional fees, or make any credits, to Deposit Account No. 06-1050, referencing Attorney Docket No. 10559-109002.

Respectfully submitted,

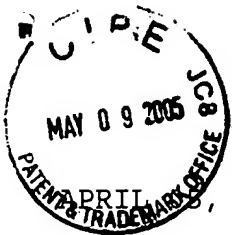
Date: May 6, 2005



William E. Hunter
Reg. No. 47,671

Fish & Richardson P.C.
Customer Number: 20985
12390 El Camino Real
San Diego, California 92130
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

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P7642



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FISH & RICHARDSON P.C.
SCOTT C. HARRIS
4225 EXECUTIVE SQUARE
SUITE 1400
LA JOLLA, CA 92037

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RECORDATION DATE: 12/21/1999

REEL/FRAME: 010502/0139
NUMBER OF PAGES: 2

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

UZELAC, LAWRENCE S.

DOC DATE: 12/16/1999

ASSIGNEE:

INTEL CORPORATION
2200 MISSION COLLEGE BLVD.
SANTA CLARA, CALIFORNIA 95052

SERIAL NUMBER: 09469986
PATENT NUMBER:

FILING DATE: 12/21/1999
ISSUE DATE:

TARA WASHINGTON, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS

1188



01-28-2000

Substitute Form PTO-1595
Attorney Docket No.: 10559/109001/P7642

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JC526 U.S. PTO

09/469986

12/21/99

Assistant Commissioner for Patents: Please record the attached original document(s) or copy(ies).

1. Name of conveying party(ies):

Lawrence S. Uzelac

Additional name(s) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Intel Corporation
2200 Mission College Blvd.
Santa Clara, CA 95052

3. Nature of conveyance:

- ☒ Assignment
☐ Merger
☐ Security Agreement
☐ Change of Name
☐ Other:

Execution Date: 12/16/99

Additional names/addresses attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed with a new application, the execution date of the application is: 12/16/99

A. Patent Application No(s):

B: Patent No(s):

09/469986

Additional numbers attached? ☐ Yes ☒ No

5. Name/address of party to whom correspondence concerning document should be mailed:

SCOTT C. HARRIS
Fish & Richardson P.C.
4225 Executive Square, Suite 1400
La Jolla, CA 92037

6. Total number of applications/patents involved: 1

7. Total fee (37 CFR §3.41): \$40

- ☒ Enclosed
☐ Authorized to charge Deposit Account.

8. Deposit Account No.: 06-1050

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Name of Person Signing

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December 21, 1999

Date

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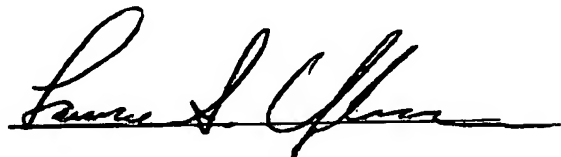
Typed Name of Person Signing Certificate

40.00 DP
09/469986
01/27/2000
01 FC

ASSIGNMENT

For valuable consideration, I, LAWRENCE S. UZELAC of Auburn, CA, hereby assign to, INTEL CORPORATION, a California corporation having a place of business at: 2200 Mission College Blvd., Santa Clara, CA 95052, and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us this day, entitled CMOS LOW LEAKAGE OPERATION OF REAL TIME CLOCK, filed _____, and assigned U.S. Serial Number _____, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

DATE: _____

12/16/99

Lawrence S. Uzelac

*** TX REPORT ***



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Attorney's Docket No.: 10559-109001
Client's Ref. No.: P7642

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MAIL STOP ASSIGNMENT FAX NO: 703/306-5995

Number of pages including this page 3

Applicant : Lawrence S. Uzelac et al. Art Unit : 2185
Serial No. : 09/469,986 Examiner : James K. Trujillo
Issued Pat. : 6,611,918 FACSIMILE COMMUNICATION
Issued Date : August 26, 2003
Filed : December 21, 1999
Title : METHOD AND APPARATUS FOR CHANGING BIAS LEVELS TO REDUCE
CMOS LEAKAGE OF A REAL TIME CLOCK WHEN SWITCHING TO A
BATTERY MODE OF OPERATION

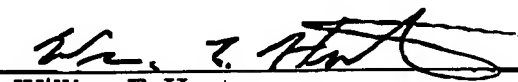
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Respectfully submitted,

Date: April 6, 2005


William E. Hunter
Reg. No. 47,671



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Number of pages including this page 3

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Serial No.	: 09/469,986	Examiner	: James K. Trujillo
Issued Pat.	: 6,611,918	FACSIMILE COMMUNICATION	
Issued Date	: August 26, 2003		
Filed	: December 21, 1999		
Title	: METHOD AND APPARATUS FOR CHANGING BIAS LEVELS TO REDUCE CMOS LEAKAGE OF A REAL TIME CLOCK WHEN SWITCHING TO A BATTERY MODE OF OPERATION		

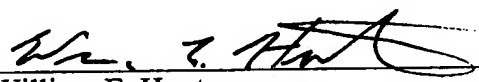
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William E. Hunter
Reg. No. 47,671

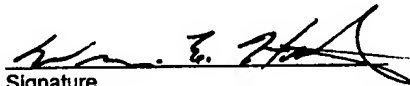
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Fax: (858) 678-5099

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3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: Execution Date: 09/18/2003	
4. Application number(s) or patent number(s): If this document is being filed with a new application, the execution date of the application is: A. Patent Application No(s).: B. Patent No(s).: 6,611,918 Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
5. Name/address of party to whom correspondence concerning document should be mailed: WILLIAM E. HUNTER Fish & Richardson P.C. 12390 El Camino Real San Diego, California 92130	6. Total number of applications/patents involved: 1 7. Total fee (37 CFR §3.41): \$40 <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to charge Deposit Account. 8. Deposit Account No.: 06-1050 Please apply any additional charges, or any credits, to our Deposit Account No. 06-1050.
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9. Statement and Signature: <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i> William E. Hunter Reg. No. 47,671 Name of Person Signing  Signature April 6, 2005 Date	
Total number of pages including coversheet, attachments and document: 2	

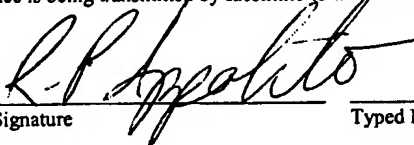
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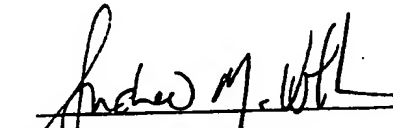
ASSIGNMENT

For valuable consideration, I, ANDREW M. VOLK of Granite Bay, California, hereby assign to: Intel Corporation, a corporation of Delaware having a place of business at: 2200 Mission College Boulevard, Santa Clara, CA 95052; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of a United States Patent, entitled METHOD AND APPARATUS FOR CHANGING BIAS LEVELS TO REDUCE CMOS LEAKAGE OF A REAL TIME CLOCK WHEN SWITCHING TO A BATTERY MODE OF OPERATION.

Country or Region/Serial No.	Filing Date
US/09/469,986 (US Pat. No. 6,611,918)	12-21-1999
US/10/625,584	7-22-2003
CN/00817542.X	06-21-2002
EP/00992848.2	07-22-2002
HK/02108424.7	06-21-2003
KR/2002-7008020	06-21-2002
SG/2002-203743-0	06-20-2003
TW/89127513	12-21-2000
PCT/US00/42597	12-05-2000

this assignment includes said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and I authorize the Assignee to apply in all countries in my name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and I agree for myself and my heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

DATE: Sept 18, 2003


ANDREW M. VOLK

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UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 11, 2004

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FISH & RICHARDSON P.C.
WILLIAM E. HUNTER
4350 LA JALLA VILLAGE DRIVE, SUITE 500
SAN DIEGO, CA 92122

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RECORDATION DATE: 10/14/2003

REEL/FRAME: 014594/0045
NUMBER OF PAGES: 2

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:
VOLK, ANDREW M.

DOC DATE: 09/18/2003

ASSIGNEE:
INTEL CORPORATION
2200 MISSION COLLEGE BOULEVARD
SANTA CLARA, CALIFORNIA 95052

SERIAL NUMBER: 10625584

FILING DATE: 07/22/2003

PATENT NUMBER:

ISSUE DATE:

TITLE: METHOD AND APPARATUS FOR REDUCING SUB-THRESHOLD OFF CURRENT FOR A
REAL TIME CLOCK CIRCUIT DURING BATTERY OPERATION

DOCKET NUMBER: 10559-109002

NOTED

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Initials: CZM

Reviewed By Billing Secretary

Initials: lmo

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014594/0045 PAGE 2

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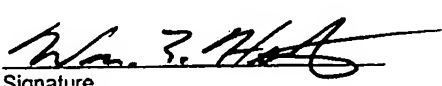
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4. Application number(s) or patent number(s): If this document is being filed with a new application, the execution date of the application is: A. Patent Application No(s): 10/625,584 B: Patent No(s): 6,611,918 Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
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9. Statement and Signature: <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i> William E. Hunter Reg. No. 47,671 Name of Person Signing  Signature 10/08/03 Date Total number of pages including coversheet, attachments and document: 2	

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October 8, 2003
Date of Deposit

Signature

Roxanne Ippolito
Typed Name of Person Signing Certificate

ASSIGNMENT

For valuable consideration, I, ANDREW M. VOLK of Granite Bay, California, hereby assign to: Intel Corporation, a corporation of Delaware having a place of business at: 2200 Mission College Boulevard, Santa Clara, CA 95052; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of a United States Patent, entitled METHOD AND APPARATUS FOR CHANGING BIAS LEVELS TO REDUCE CMOS LEAKAGE OF A REAL TIME CLOCK WHEN SWITCHING TO A BATTERY MODE OF OPERATION.

Country or Region/Serial No.	Filing Date
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CN/00817542.X	06-21-2002
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KR/2002-7008020	06-21-2002
SG/2002-203743-0	06-20-2003
TW/89127513	12-21-2000
PCT/US00/42597	12-05-2000

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DATE:

Sept 18, 2003


ANDREW M. VOLK

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Client's Ref. No.: P7642C

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Number of pages including this page 3

Applicant : Lawrence S. Uzelac, et al. Art Unit : 2116
Serial No. : 10/625,584 Examiner : Unkown
Filed : July 22, 2003 FACSIMILE COMMUNICATION
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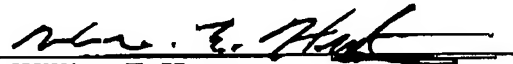
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Date: April 6, 2005


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Reg. No. 47,671

PTO Customer No. 20985
Fish & Richardson P.C.
12390 El Camino Real

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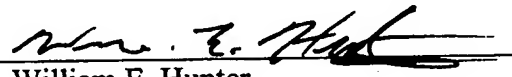
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William E. Hunter
Reg. No. 47,671

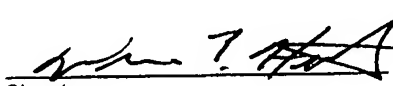
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5. Name/address of party to whom correspondence concerning document should be mailed: WILLIAM E. HUNTER Fish & Richardson P.C. 12390 El Camino Real San Diego, California 92130	6. Total number of applications/patents involved: 1 7. Total fee (37 CFR §3.41): \$40 <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to charge Deposit Account. 8. Deposit Account No.: 06-1050 Please apply any additional charges, or any credits, to our Deposit Account No. 06-1050.
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9. Statement and Signature: <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i> William E. Hunter Reg. No. 47,671 Name of Person Signing  Signature April 6, 2005 Date	
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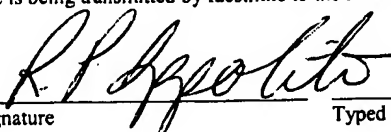
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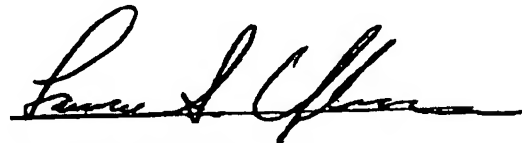
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ASSIGNMENT

For valuable consideration, I, LAWRENCE S. UZELAC of Auburn, CA, hereby assign to, INTEL CORPORATION, a California corporation having a place of business at: 2200 Mission College Blvd., Santa Clara, CA 95052, and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us this day, entitled CMOS LOW LEAKAGE OPERATION OF REAL TIME CLOCK, filed _____, and assigned U.S. Serial Number _____, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

DATE: 12/16/99

Lawrence S. Uzelac